

SPECIFICATIONS
 OPERATING CURRENT: ~50mA @ 12VDC
 SLEEP CURRENT: ~80mA @ 12VDC
 VOLTAGE RANGE: 7-30VDC
 OPERATING TEMPERATURE: -40°C to +70°C
 (-40°F to +160°F)

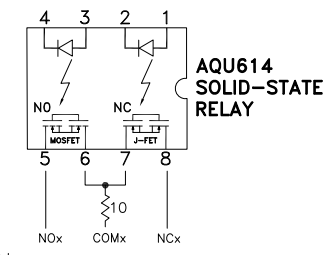
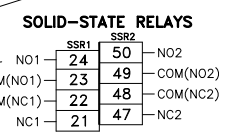
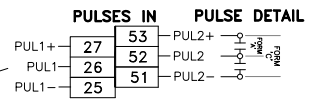
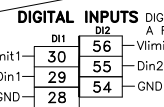
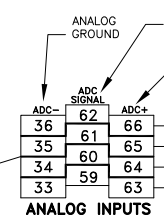
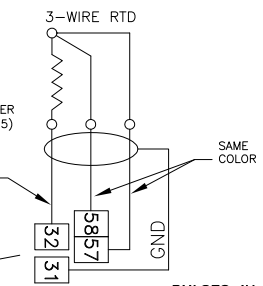
JUMPER CONFIGURATIONS

JP1	NOT INSTALLED - NORMAL OPERATION
JP2	UP - USED FOR PROGRAMMING THE MAC ADDRESS NOTE: MAC ADDRESS CAN ONLY BE PROGRAMMED ONCE. DO NOT INSTALL JUMPER AFTER THE MAC ADDRESS HAS BEEN PROGRAMMED
JP3	NOT INSTALLED
JP4	NOT INSTALLED
JP5	NOT INSTALLED
JP6	NOT INSTALLED
JP7	NOT INSTALLED
JP8	NOT INSTALLED
JP9	NOT INSTALLED
JP10	NOT INSTALLED
JP11	NOT INSTALLED
JP12	WITH BOARD POWERED DOWN, CLEARS DATABASE NORMAL OPERATING CONDITION
JP13	Vbat (SWITCHED Vbat ~ 150mA MAX.) PLACED ON TERMINALS 63 & 64 5V (5V, 30mA MAX.) PLACED ON TERMINALS 63 & 64 TERMINALS 63 & 64 ARE <u>NOT</u> INTERNALLY POWERED
JP14	NOT INSTALLED
JP15	Vbat (SWITCHED Vbat ~ 150mA MAX.) PLACED ON TERMINALS 65 & 66 5V (5V, 30mA MAX.) PLACED ON TERMINALS 65 & 66 TERMINALS 65 & 66 ARE <u>NOT</u> INTERNALLY POWERED

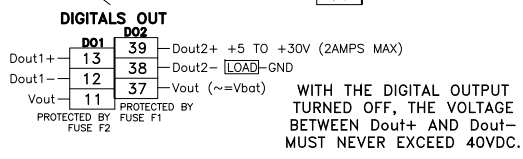
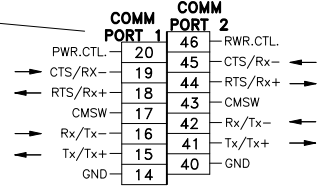
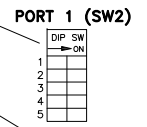
E3 EXTENDER CONNECTION EXPANSION PORT J10

R6, R7, R9, & R10
 DROPPING RESISTORS FOR ANALOG CHANNELS 1-4. 250 OHM (FOR 4-20 mA) BETWEEN EACH ANALOG 'ADC' (SIGNAL) TERMINAL AND GROUND. THESE RESISTORS WILL BE REMOVED FOR 0-5VDC SIGNALS.

RTD INPUT DETAIL



COMMUNICATIONS DIPSWITCH POSITIONS
 1 - ON - RS232 ENABLED
 OFF - RS485 ENABLED
 2 - ON - TIES Rx+ TO Tx+ & Tx-
 OFF - DISCONNECTS Rx+ & Tx-
 3 - ON - TIES Rx- TO Tx-
 OFF - DISCONNECTS Rx- & Tx-
 4 - ON - CONNECTS TERMINATING RESISTOR
 OFF - DISCONNECTS TERMINATING RESISTOR
 5 - ON - CONNECTS TERMINATING RESISTOR
 OFF - DISCONNECTS TERMINATING RESISTOR



DIGITAL ADDRESSING

Hardware Function	Description	Port Bit
DIN1	Digital In 1	1.1
DIN2	Digital In 2	1.2
DOUT1	Digital Out 1	2.1
DOUT2	Digital Out 2	2.2
SSR1_A	Solid State Relay 1 Form A	3.1
SSR1_B	Solid State Relay 1 Form B	3.2
SSR1_AB	Solid State Relay 1 Form C	3.3
SSR2_A	Solid State Relay 2 Form A	3.4
SSR2_B	Solid State Relay 2 Form B	3.5
SSR2_AB	Solid State Relay 2 Form C	3.6
PUL1	Pulse Input 1 (CONFIGURED AS A DIGITAL INPUT)	5.1
PUL2	Pulse Input 2 (CONFIGURED AS A DIGITAL INPUT)	5.2
PWRCTRL1	RS232 Power Control 1	6.1
PWRCTRL2	RS232 Power Control 2	6.2
CMSW1	RS232 Com Switch 1	7.1
CMSW2	RS232 Com Switch 2	7.2

PULSE ADDRESSING

Hardware Function	Description	Port Bit
PUL1	Pulse Input 1	1.1
PUL2	Pulse Input 2	1.2

I²C ADDRESSING

I ² C Addresses	Auxiliary Device Address	Description
101	1	THE FIRST FOUR I ² C ADDRESSES ARE RESERVED FOR E-ESP's
102	2	
103	3	
104	4	
105	5	I ² C ADDRESSES 105-115 ARE FOR ALL OTHER DEVICES ON THE I ² C BUS SUCH AS E-AO AND E-DIGITAL
106	6	
107	7	
108	8	
109	9	
110	10	
111	11	
112	12	
113	13	
114	14	
115	15	

ANALOG ADDRESSING

Address	Description
1	Analog Input 1 (ADC1)
2	Analog Input 2 (ADC2)
3	Analog Input 3 (ADC3)
4	Analog Input 4 (ADC4)
9	Temperature 1 (RTD)
11	Ambient Temperature
12	Supply Volts

E3 PROCESSOR BOARD

PART NO. 9010530 (FULL BOARD)

WARNING:
 DO NOT CHANGE ANY JUMPER CONFIGURATION, CONNECT, OR DISCONNECT ANY BATTERIES OR ELECTRICAL DEVICES UNLESS THE ATMOSPHERE IS KNOWN TO BE NON-HAZARDOUS.
****NOTE****
 THIS IS NOT THE UL CONTROL DRAWING FOR THIS BOARD. SEE THAT DRAWING IN THE UL DOCS FOLDER.

5						OLD DWG #	N/A
4						TOLERANCES - UNLESS OTHERWISE SPECIFIED	
3						INCHES	ANGLES ± 1°
2	DRH	SMC	XXX	XXX	03-MAY-16	.X±	MILLIMETERS
1	DRH	SMC	XXX	XXX	01-MAR-16	.XX± .02"	X±
REV	DRFT	ENGR	CHKR	ECN#	DATE	.XX± .005"	X.X±
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DWG. RELEASE ECN #:						N/A	

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TITLE: **E3 PROCESSOR GENERAL INFORMATION SHEET**
 PROJECT: INTERNAL

DRFT: JVF 05-OCT-15
 ENGR: SMC 05-OCT-15
 CHKR: XXX XX-XXX-XX

DRAWING NUMBER
 9010530 GIS

SCALE: NTS SHEET: 1